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Valid OWB control itemsory	Application / Conf. No.	10/	792,153 / 4867								
TRANSMITTAL	Filing Date	Ma	March 02, 2004								
FORM	First Named Inventor	Ro	Robert E. Eccles Unknown								
(to be used for all correspondence after Initial filtr	Examiner Name	Un									
Mail Stop: Office of Initial Patent Examination	Art Unit	28	2825								
Express Mail VIIA RACSIMILE	Patent No.	70.	703-746-9195								
Receipt No. Total Number of Pages in This Submission	5	Attorney Docket Number	X-	X-1270 US							
ENCLOSURES (check all that apply)											
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Information Disclosure Statement	Powe Chan Addr			Request for Corrected Filing Receipt; copy of filing receipt; copy of Application Data							
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Response to Missing Parts/ Incomplete Application											
Response to Missing Parts under 37 CFR 1,52 or 1,53											
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Firm or Customer Number		24309		77 Alimber 37 653							
Atto: Kim Kanzaki	<u> </u>	(O.storner Number)		Reg. Number 37,652							
Signature	<u> </u>		,								
Date June 25,/2004/	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040										
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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant:

Robert E. Eccles

Assignee:

Xilinx, Inc.

Title:

"Method and Apparatus for Design Verification with

Equivalency Checking"

Serial No.:

10/792,153

File Date: 03-02-04

Examiner:

Unknown

Art Unit: 2825

Docket No.:

x-1270 US

VIA FACSIMILE 703-746-9195

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REQUEST FOR CORRECTED FILING RECEIPT

Dear Sir:

Applicant requests a corrected Filing Receipt in the above identified patent application, to include the following priority information:

This application claims priority to provisional application no. 60/452,143 which was filed on March 4, 2003. A copy of the Application Data Sheet filed with the above-referenced application which includes the claimed priority is enclosed.

No fee is due with this communication but the Commissioner is hereby authorized to charge any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

Kim Karzaki

Afterney for Applicant

Reg/No. 37,652

I hereby certify that this correspondence is being sent via Facsimile to 703-746-9195 on June 25, 2004.

Pat Slaback

Name

Signature





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CONFIRMATION NO. 4867

FILING RECEIPT 24309 XILINX, INC ATTN: LEGAL DEPARTMENT 2100 LOGIC DR **SAN JOSE, CA 95124**

*OC0000000129213121

Date Mailed: 06/10/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filling Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Robert E. Eccles, San Jose, CA;

Assignment For Published Patent Application

Xilinx, Inc., San Jose, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 06/10/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Priority to:

Method and apparatus for design verification with equivalency check

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JUN 1 6 2004

XILINX INC. DEPT.